Announcing Ashling RiscFree™ Toolchain for RISC-V

RISC-V Tools Ecosystem Gets a Boost with Ashling RiscFree Product Line

RISC-V SUMMIT, Santa Clara, CA – December 4, 2018. Ashling (a subsidiary of the NeST Group) today announced the market release of its latest embedded tools technologies supporting the RISC-V user base, Ashling RiscFree™ C/C++ for RISC-V. It is a fully integrated development tool environment which includes IDE, compiler, debugger, and a JTAG probe ready to use with most RISC-V based core.

> "We are happy to introduce the first commercially supported toolset for RISC-V backed by a full service offering to enable any custom ISA extensions to RISC-V cores." said Guy Rabbat, President and CEO of Ashling.

Ashling is fully committed to RISC-V and is expanding and adapting its offerings to all aspects of the Ecosystem based on its extensive experience of the last 25 years and its customers' requirements. As RISC-V products become more widespread and the RISC-V ecosystem continues to rapidly expand, RISC-V will move from novelty to ubiquity and Ashling will be there to help make it happen.

Ashling RiscFree™ comes with full out-of-the-box support for SiFive's RISC-V Core IP products. SiFive, founded by the inventors of RISC-V, is the leading RISC-V semiconductor company with the world's first Linux-capable, multi-core, RISC-V Silicon the Freedom U540 and its HiFive Unleashed development board. Ashling's RiscFree™ Toolchain supports SiFive's HiFive Unleashed development board as well as SiFive's FPGA based Core IP evaluation platforms with ready-to-use projects.

> "As we see strong growth in the adoption of RISC-V across our Core Series products, we are excited to see Ashling's development tools added to the RISC-V ecosystem and supporting SiFive's RISC-V based Core IP products." said Jack Kang, SiFive's VP of Product Marketing.

Ashling RiscFree™ tools offering will also include the popular Ashling Opella for RISC-V Debug probe and will also work with other JTAG probes in the market. The company will be expanding its cross-compiler development team and expertise to offer embedded tools customization to support any RISC-V ISA extensions.

About Ashling

Ashling is a world-class technology partner offering integrated solutions, tools, and design services that are at the heart of the embedded environment. Through its close cooperation with leading semiconductor vendors, Ashling has become a leader in the Embedded Software Development Tools market. Its solutions are used by engineers across a diverse range of applications from automotive and aerospace to healthcare and IoT, and are supported with offices in several countries USA, UK, Ireland,

Germany, India and a distribution network in 10 other countries. Ashling tools & solutions support RISC-V, ARM, ARC and MIPS.

For more information on Ashling see: http://www.ashling.com and on our Tools as a Service (TaaS™) model see here: http://www.ashling.com/services-taas/

About RISC-V

RISC-V open architecture ISA is under the governance of the RISC-V Foundation.

Visit https://riscv.org for more details

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