Ashling announces RiscFree™ C/C++ Toolchain for Microchip PolarFire® SoC FPGA RISC-V Processors

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Silicon Valley, Calif. USA - Ashling announced today that Ashling's ***RiscFree*** Toolchain for RISC-V will provide specific software development support for Microchip Technology's PolarFire® SoC RISC-V-based processors.

The PolarFire SoC FPGA family delivers a combination of low power consumption, thermal efficiency and defense grade security for smart, connected systems and is the first system on chip (SoC) field-programmable gate array (FPGA) with a deterministic, coherent RISC-V CPU cluster and a deterministic L2 memory subsystem enabling Linux and real-time applications.

> *"As RISC-V ISA based development and deployment picks up speed, we are happy to see Ashling, one of the most prominent RISC-V toolchain providers, adding support for our PolarFire SoC FPGA. Ashling **RiscFree** will be a strong addition to our diverse ecosystem of modern tools and solutions that benefit our growing customer base and will result in faster time-to-market for our end customers,"* said **Krishnakumar, Senior Manager Product Marketing and Ecosystem, in Microchip's FPGA business unit.**

RiscFree is Ashling's Integrated Development Environment (IDE) including a compiler and debugger and provides software development and debug support for PolarFire SoC FPGA series. Since its introduction, Ashling's ***RiscFree*** toolchain has been steadily building market share within the embedded tools market and is particularly strong in the RISC-V market where its ease-of-use, broad functionality, plug-in architecture and upcoming Real-time Trace support have made it the go-to choice for 32-bit and 64-bit RISC core software development for the aerospace and defense industries.

> *"We are happy to announce our support for Microchip PolarFire SoC RISC-V based FPGA and that our **RiscFree** is now part of Microchip's ecosystem of development and debug. We are confident **RiscFree** support will offer the user base of Microchip's PolarFire FPGAs a leading Toolchain which will help in speeding up the deployment of any PolarFire FPGA-powered solution,"* said **Hugh O'Keeffe, CEO of Ashling**.

For more information on Ashling's ***RiscFree*** see: https://www.ashling.com/ashling-riscv/ and for details on Microchip's PolarFire SoC FPGA see: https://www.microchip.com/en-us/products/fpgas-and-plds/system-on-chip-fpgas/polarfire-soc-fpgas

About Ashling

Ashling have been a leading provider of Embedded Development Tools & Services since 1982 with design centres in Limerick Ireland and Kochi India and sales and support offices in Europe, Asia Pacific, the Middle East and America. Visit www.ashling.com for more details.

About RISC-V

The RISC-V open architecture ISA is under the governance of the RISC-V International. Visit https://riscv.org for more details.

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